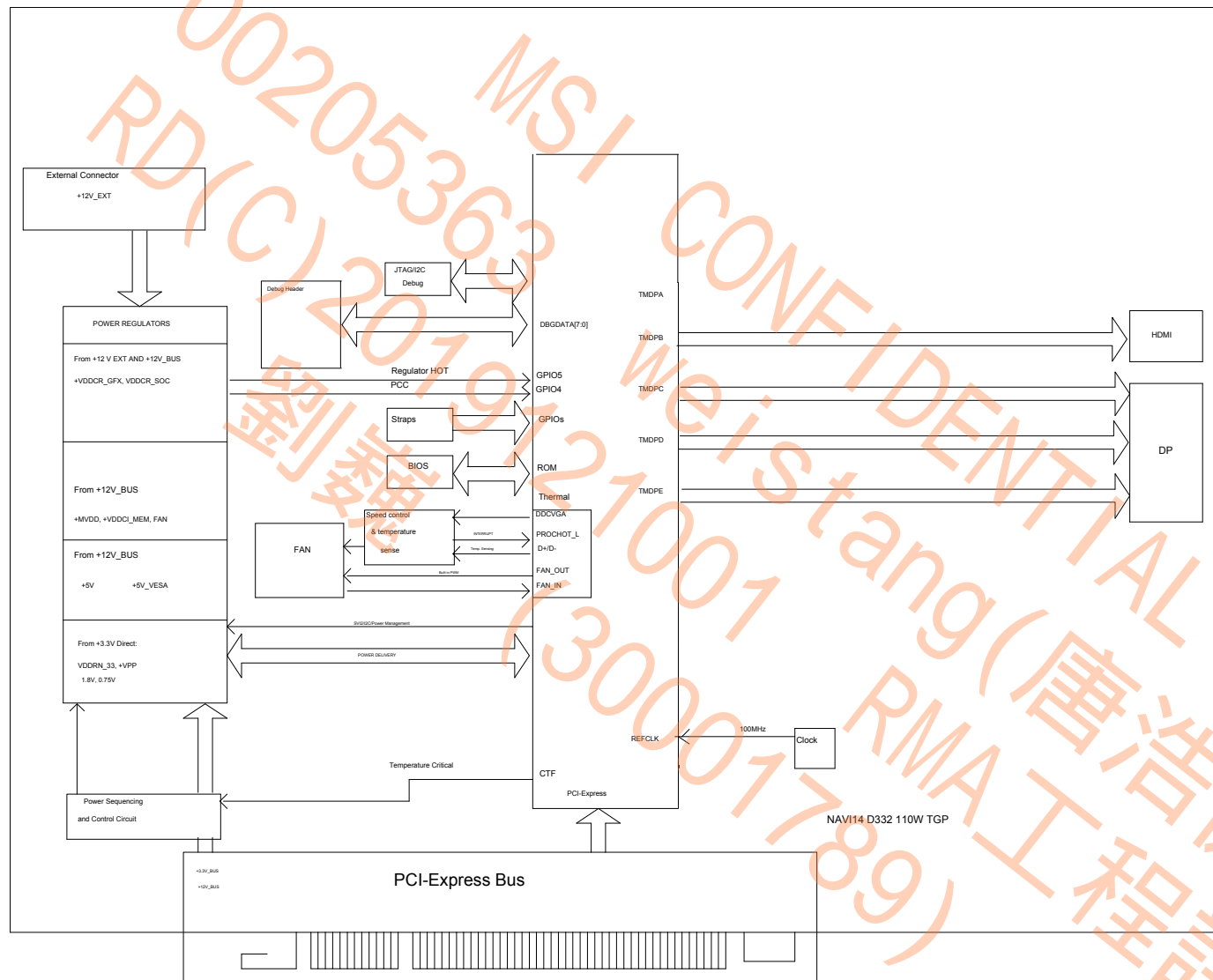


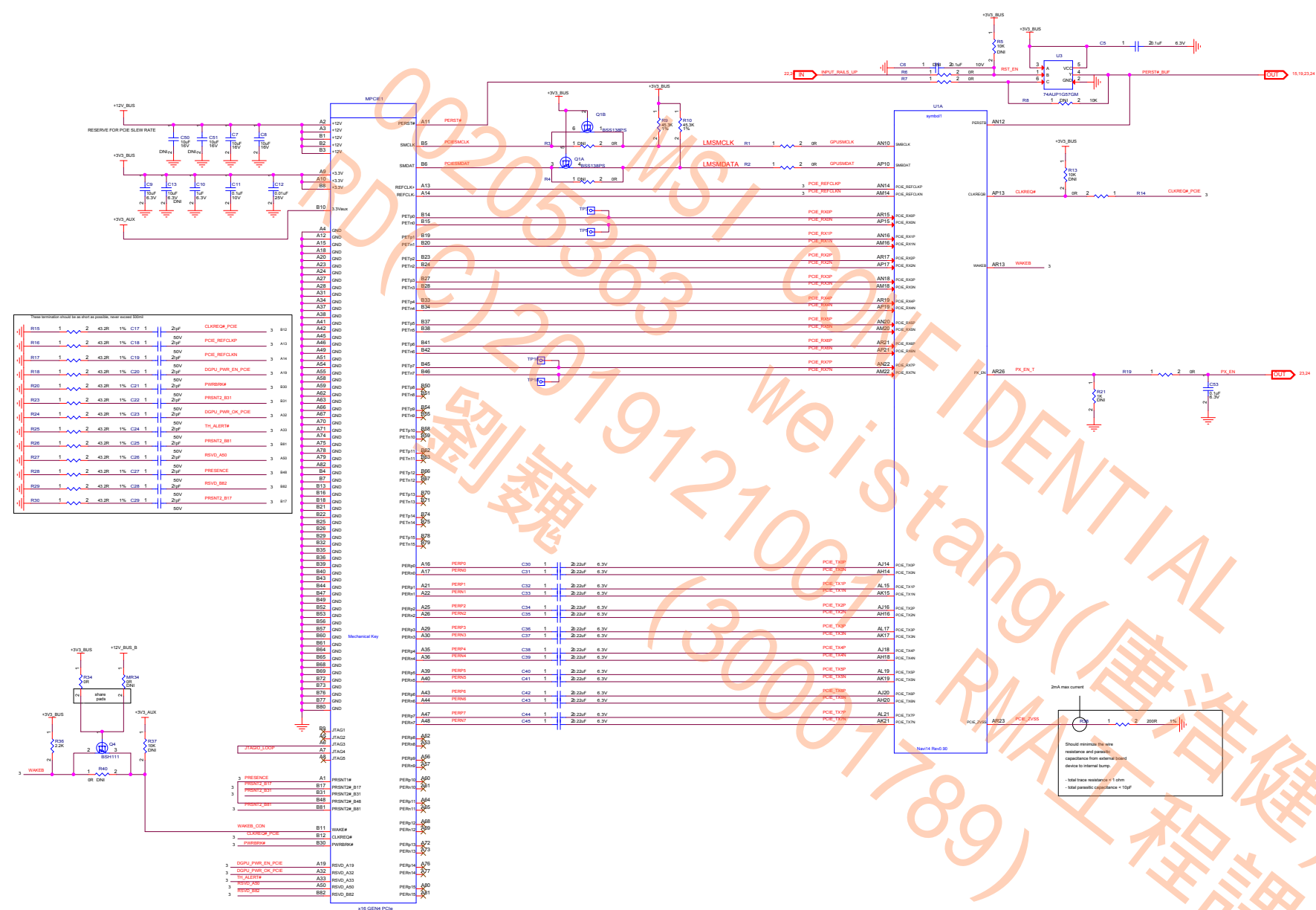
NAVI14 GDDR6 4PCS,8L 3xDPs +1xHDMI



105-D332xx-00B RevB Desktop board(110W TGP)

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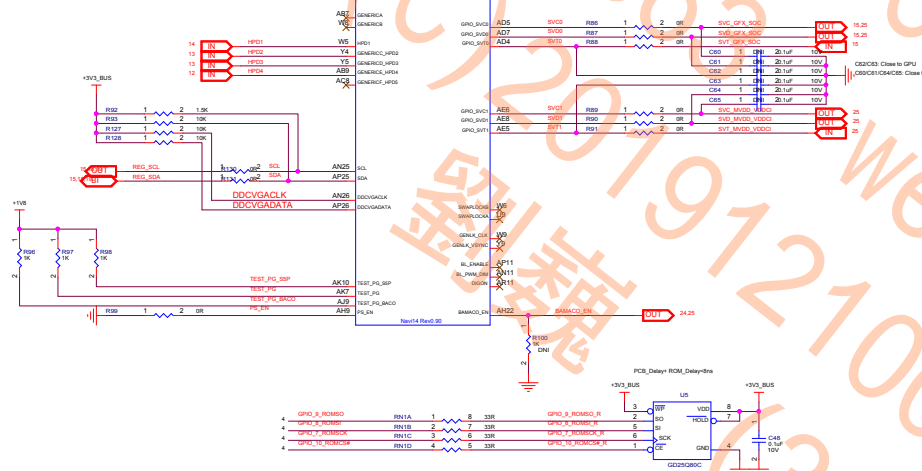
SHEET NO.	SHEET NAME	SHEET NO.	SHEET NAME
1	TOC	26	DEBUG CIRCUITS
2	BLOCK DIAGRAM	27	REVISION HISTORY
3	NAVI14 - PCIe Interface		
4	NAVI14 - GPIO, EXTRA		
5	NAVI14 - XTAL		
6	NAVI14 - POWER and GND		
7	NAVI14 - Decaps		
8	NAVI14 - MEM CH AB		
9	NAVI14 - MEM CH CD		
10	GDDR6 x16 - MEM CH AB		
11	GDDR6 x16 - MEM CH CD		
12	NAVI14 - TMDPAB - USB,HDMI		
13	NAVI14 - TMDPCD - DP,DP		
14	NAVI14 - TMDPE - DP		
15	REG VDDGFX/SOC CNTL		
16	REG VDDGFX		
17	REG VDDSOC		
18	REG PCC		
19	REG VDDCI/MVDD CNTL		
20	REG VDDCI/MVDD		
21	REG - 0V75		
22	LDO 1VB/VPP/SV_VESA		
23	MECHANICAL AND THERMAL		
24	POWER MANAGEMENT		
25	SVIZ		





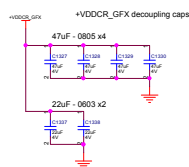
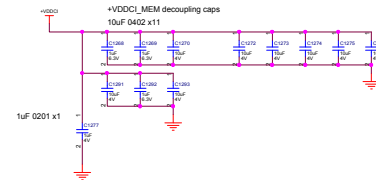
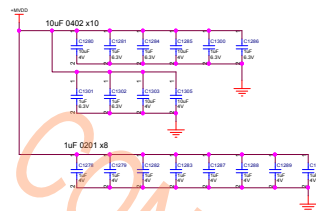
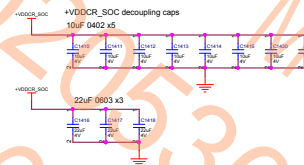
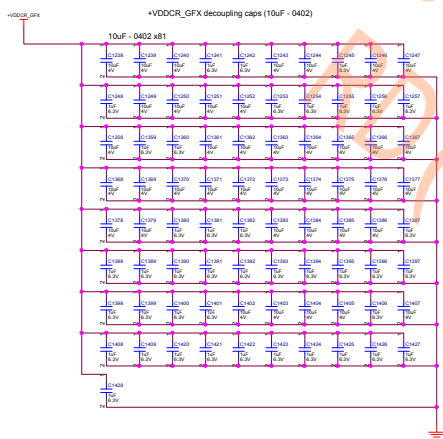
SYMBOL LEGEND	
DN	DO NOT INSTALL
S or #	ACTIVE LOW
BUO	BRING UP ONLY
	DIGITAL GROUND
	ANALOG GROUND

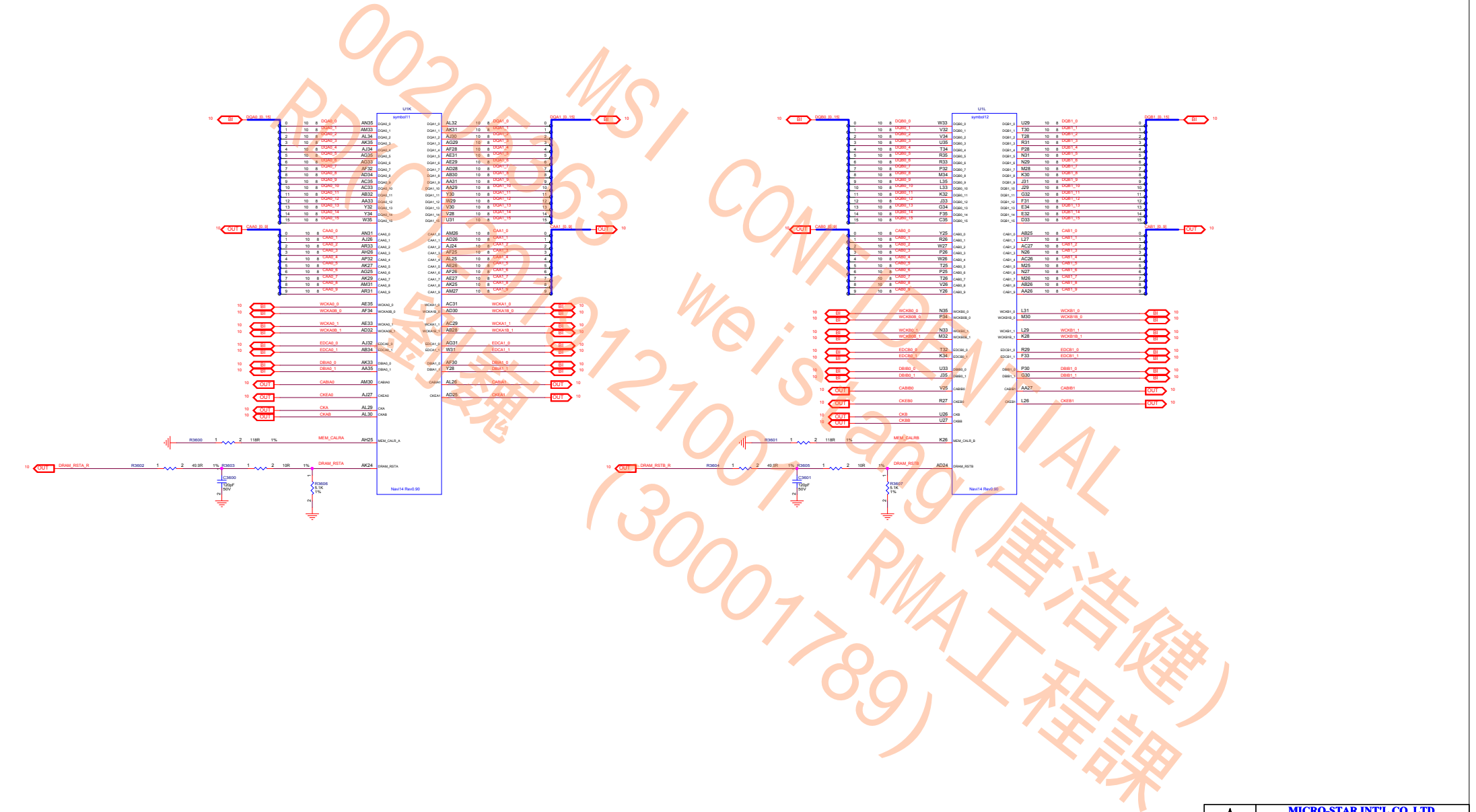
7-BIT I2C ADDRESS	8-BIT I2C ADDRESS	FUNCTION	I2C BUS	DEVICE
0x30	0x60	VDDCR_GFX/SOC	SCL/SDA	U501IR35217
0x22	0x44	MVDDIOVDDCI	SCL/SDA	U701NCP81022
0x53	0xA6	CPQReserved	SCL/SDA	U4601/CS9B001
0x48	0x90	G6 Temperature	DDCVGACLK/DA	U4000/TMP102A
0x4C	0x98	EXT SENSOR	DDCVGACLK/DA	U4001/LM98163

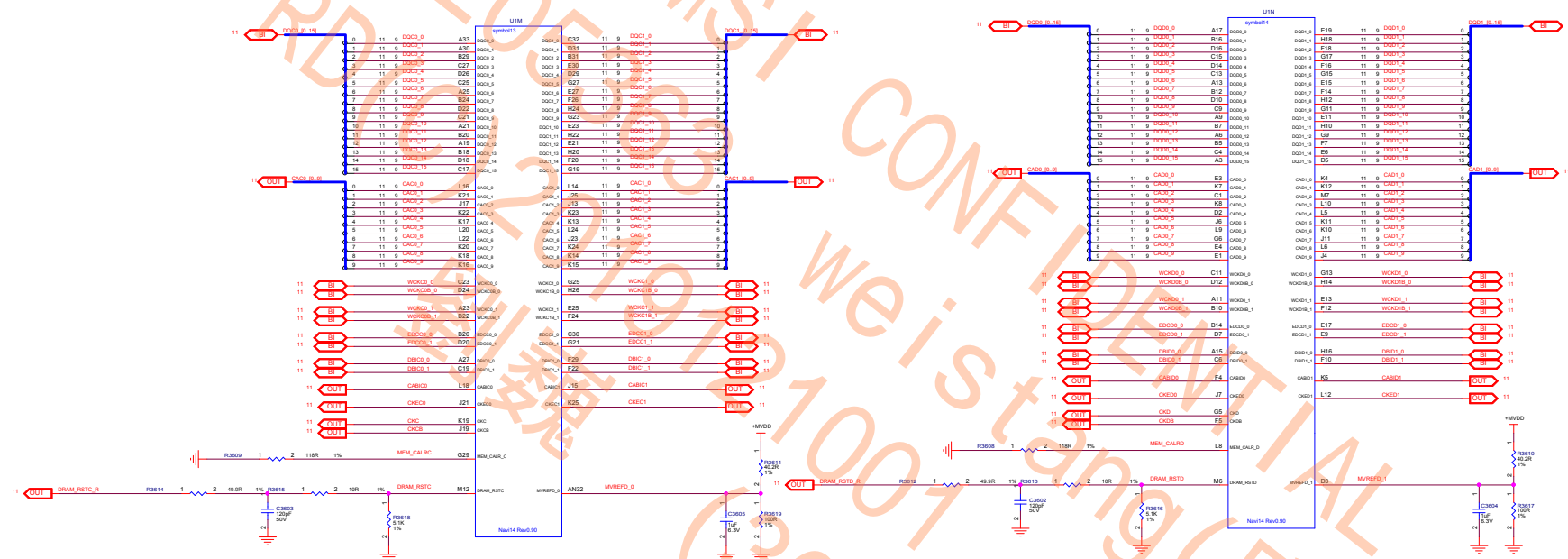


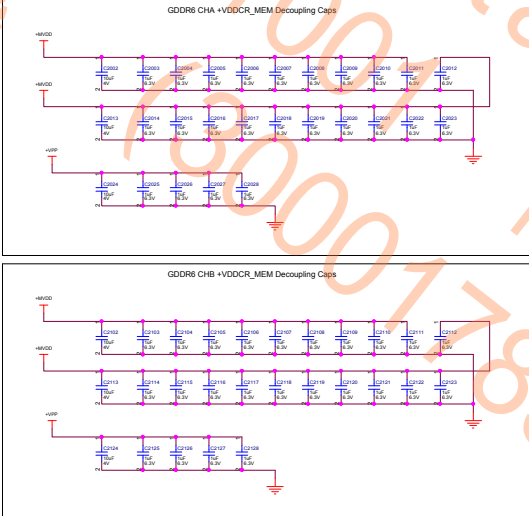
User	+VDD_BIAS	+VDD_BIAS	Internal Default Value	Definition
BIF	R851 1 10K 2 10K	R852 1 1 2 10K	0	STRAP_BIF_GEN4_DIS_A 0: PC1a GEN4 supported 1: PC1a GEN4 not supported
	R853 1 10K 2 10K	R854 1 1 2 10K	0	PINSTRAP_BIF_CLK_PM_EN 0: CLARESC power management capability is disabled 1: CLARESC power management capability is enabled
	R855 1 10K 2 10K	R857 1 1 2 10K	0	PINSTRAP_BIF_LC_TX_SWING 0: Full swing mode 1: Reduced swing mode(half swing)
	R858 1 10K 2 10K	R859 1 1 2 10K	0	PINSTRAP_BIF_VGA_DIS 0: VGA controller capacity enabled 1: The device won't be recognized as the system's VGA controller
DCE	R860 1 10K 2 10K	R861 1 1 2 10K	0	PINSTRAP_AUD_PORT_CONN[2:0] Number of audio-capable display outputs
	R862 1 10K 2 10K	R863 1 1 2 10K	0	4: All endpoints connected 3: 4 endpoints connected 2: 3 endpoints connected 1: 2 endpoints connected 0: No endpoints connected
	R864 1 10K 2 10K	R865 1 1 2 10K	0	4: All endpoints connected 3: 4 endpoints connected 2: 3 endpoints connected 1: 2 endpoints connected 0: No endpoints connected
	R866 1 10K 2 10K	R867 1 1 2 10K	0	Default(PINSTRAP1.PINSTRAP1A.PINSTRAP1B) = 000
Platform	R868 1 10K 2 10K	R869 1 1 2 10K	0	PINSTRAP_AUD[1:0] 1: Audio for DisplayPort only 2: Audio for DisplayPort and HDMI if single is detected 3: Audio for both DisplayPort and HDMI Default(PINSTRAP12.CP011) = 11
	R870 1 10K 2 10K	R871 1 1 2 10K	0	PINSTRAP_FIG[1:0] - No definition on NV
	R872 1 10K 2 10K	R873 1 1 2 10K	0	PINSTRAP_MVDD_FB_DIVIDER_CONFIG 0: Divider does not exist 1: Divider exists
	R874 1 10K 2 10K	R875 1 1 2 10K	0	PINSTRAP_BFI_MEM_AP_SIZE[2:0] Or PINSTRAP_ROM_CONFIG[2:0] 100: 0x20B1 (01) ADDR00A 101: 0xB0E7 (01) ADDR00A 102: 0xB0E7 (01) ADDR00A 103: 0xB0E7 (01) ADDR00A 104: 0xB0E7 (01) ADDR00A 105: 0xB0E7 (01) ADDR00A 106: 0xB0E7 (01) ADDR00A 107: 0xB0E7 (01) ADDR00A 108: 0xB0E7 (01) ADDR00A 109: 0xB0E7 (01) ADDR00A 110: 0xB0E7 (01) ADDR00A 111: 0xB0E7 (01) ADDR00A Default(PINSTRAP2.PINSTRAP1.PINSTRAP1B) = 101
SMU	R876 1 10K 2 10K	R877 1 1 2 10K	1	PINSTRAP_SMBUS_ADDR 0: Default 1: Qw42
	R878 1 10K 2 10K	R879 1 1 2 10K	0	PINSTRAP_BIOS_ROM_EN 0: Disable the external BIOS ROM device 1: Enable the external BIOS ROM device
	R880 1 10K 2 10K	R881 1 1 2 10K	1	PINSTRAP_BIOS_ROM_EN 0: Disable the external BIOS ROM device 1: Enable the external BIOS ROM device
	R882 1 10K 2 10K	R883 1 1 2 10K	0	PINSTRAP_BIOS_ROM_EN 0: Disable the external BIOS ROM device 1: Enable the external BIOS ROM device
SMU	R884 1 10K 2 10K	R885 1 1 2 10K	1	PINSTRAP_BIOS_ROM_EN 0: Disable the external BIOS ROM device 1: Enable the external BIOS ROM device
	R886 1 10K 2 10K	R887 1 1 2 10K	0	PINSTRAP_BIOS_ROM_EN 0: Disable the external BIOS ROM device 1: Enable the external BIOS ROM device
	R888 1 10K 2 10K	R889 1 1 2 10K	0	PINSTRAP_BIOS_ROM_EN 0: Disable the external BIOS ROM device 1: Enable the external BIOS ROM device
	R890 1 10K 2 10K	R891 1 1 2 10K	0	PINSTRAP_BIOS_ROM_EN 0: Disable the external BIOS ROM device 1: Enable the external BIOS ROM device



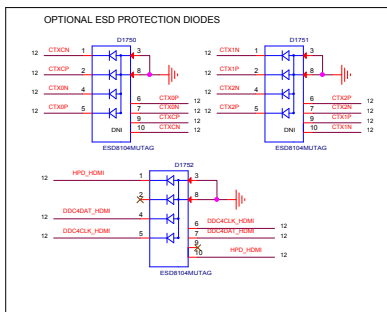
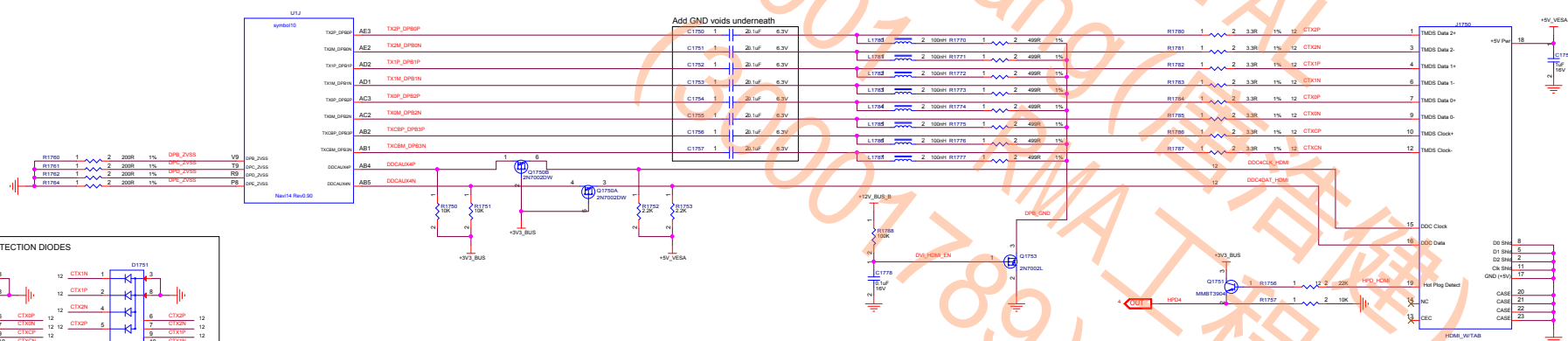
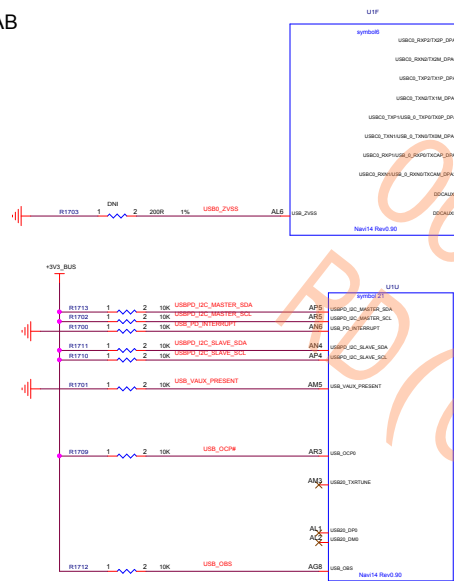


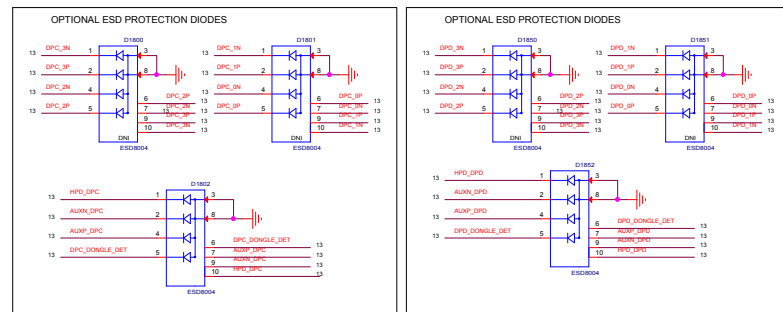
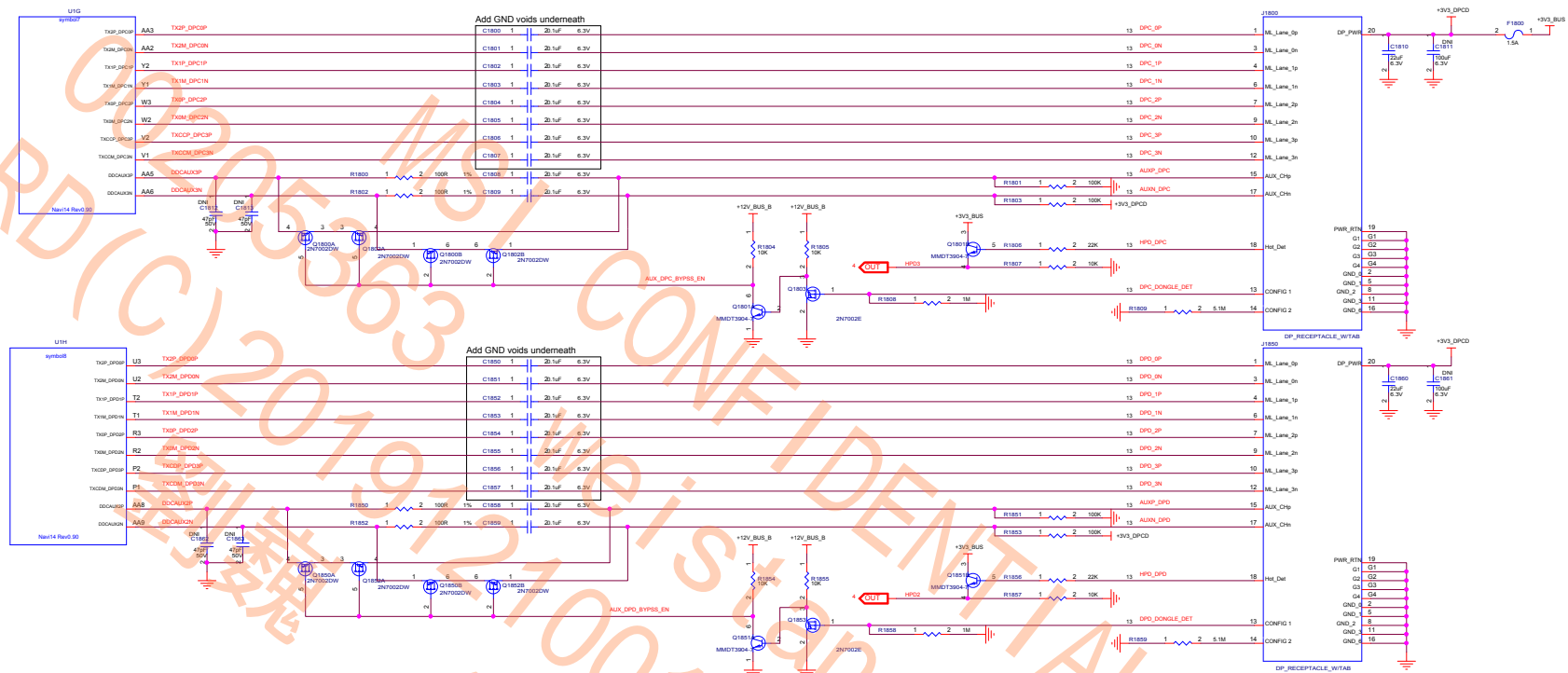


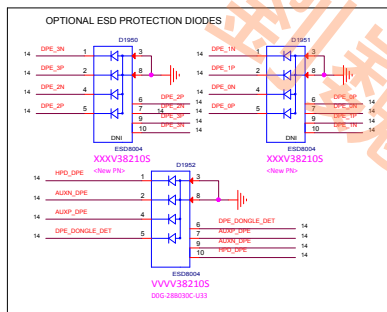


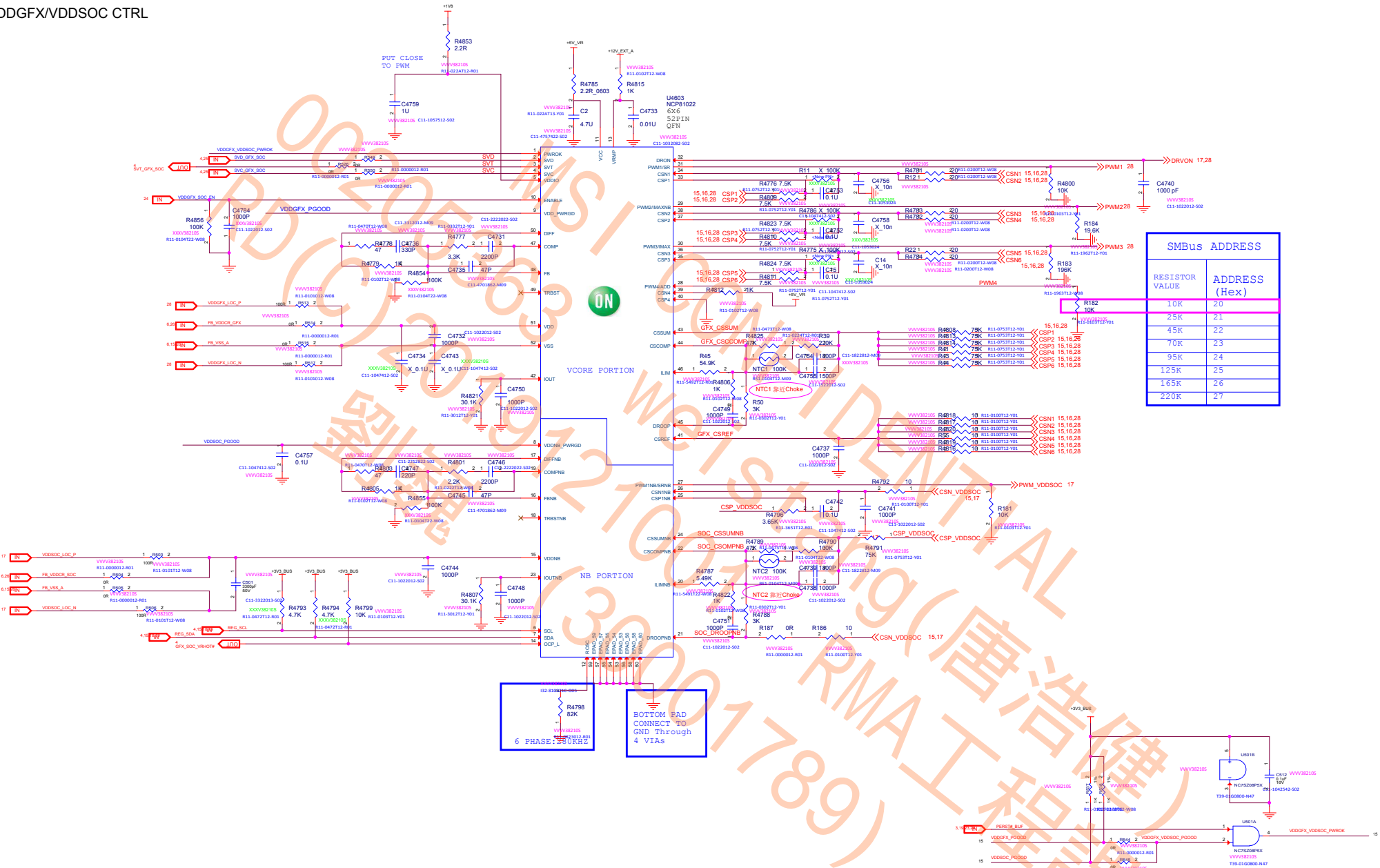


(12) TMDP AB



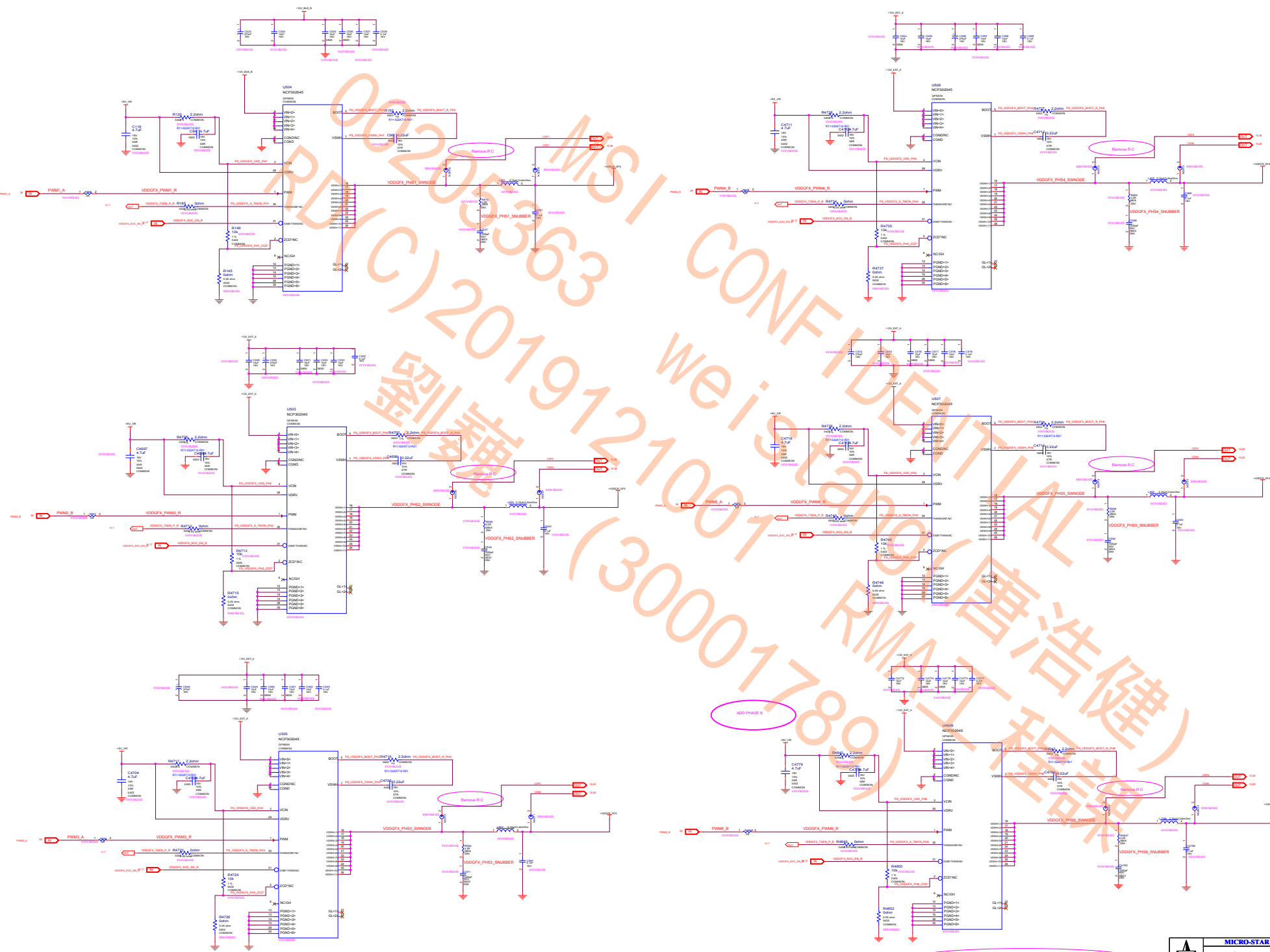


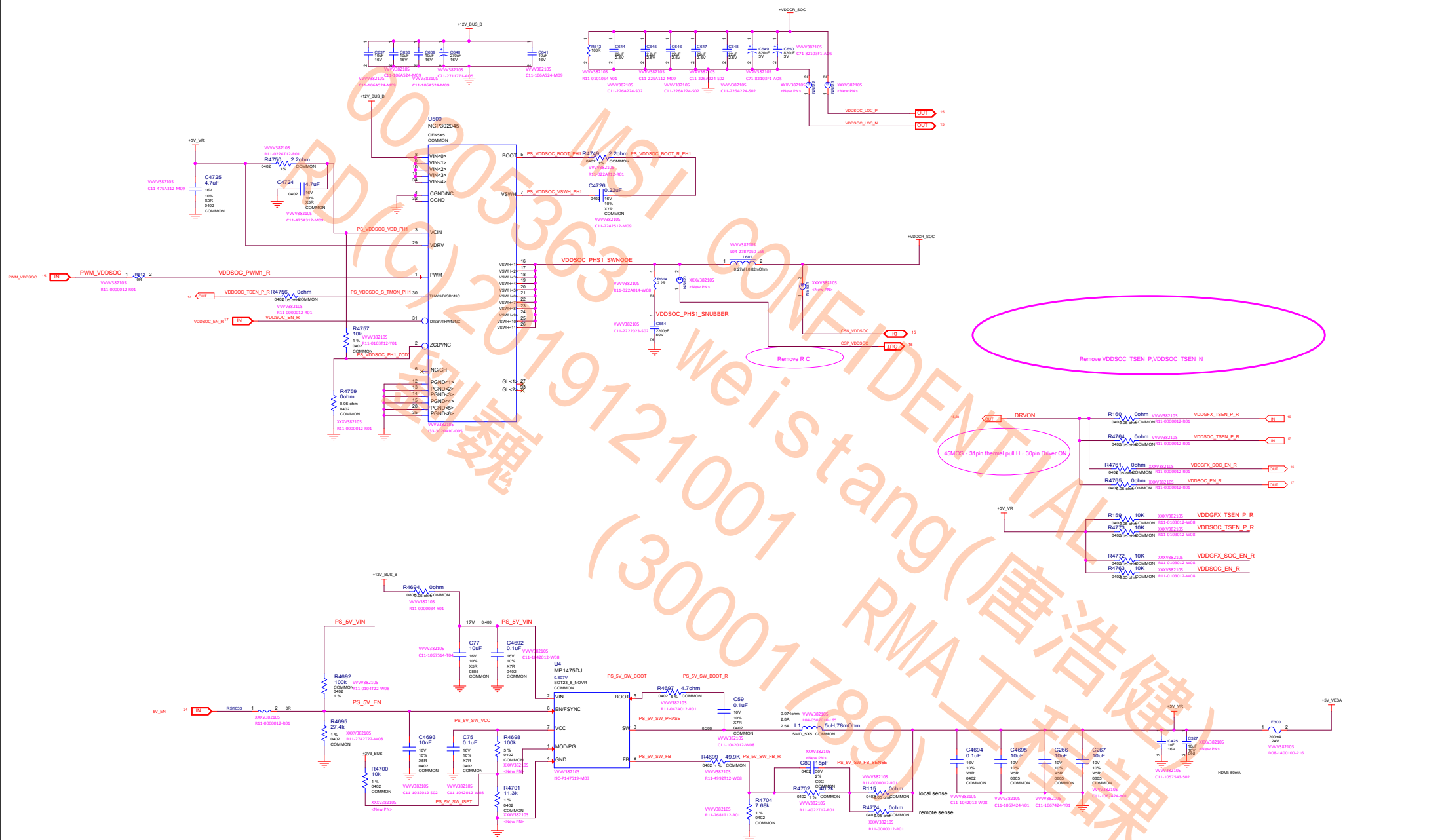


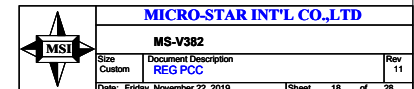


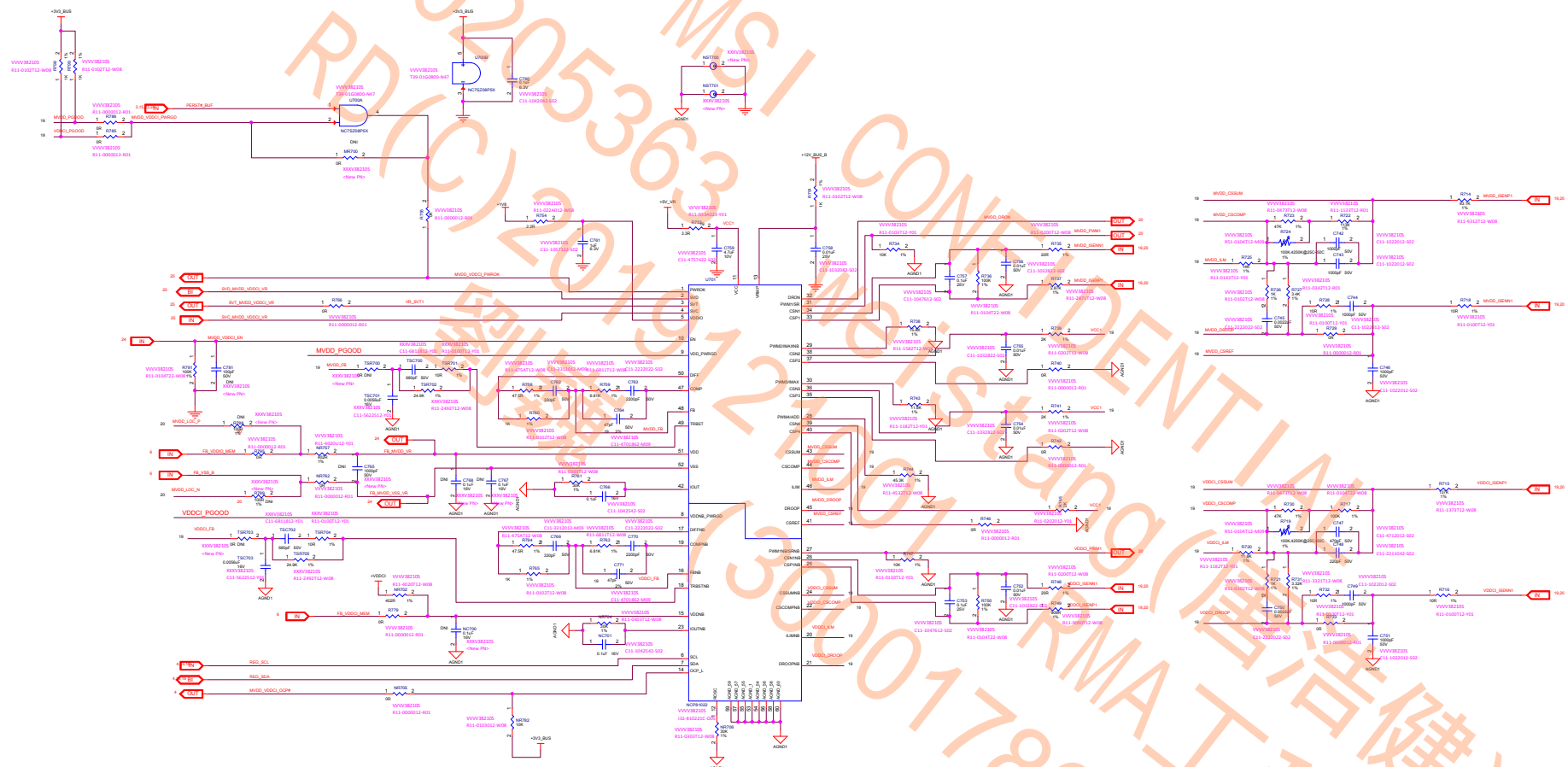
SMBus ADDRESS	
RESISTOR VALUE	ADDRESS (Hex)
10K	20
25K	21
45K	22
70K	23
95K	24
125K	25
165K	26
220K	27

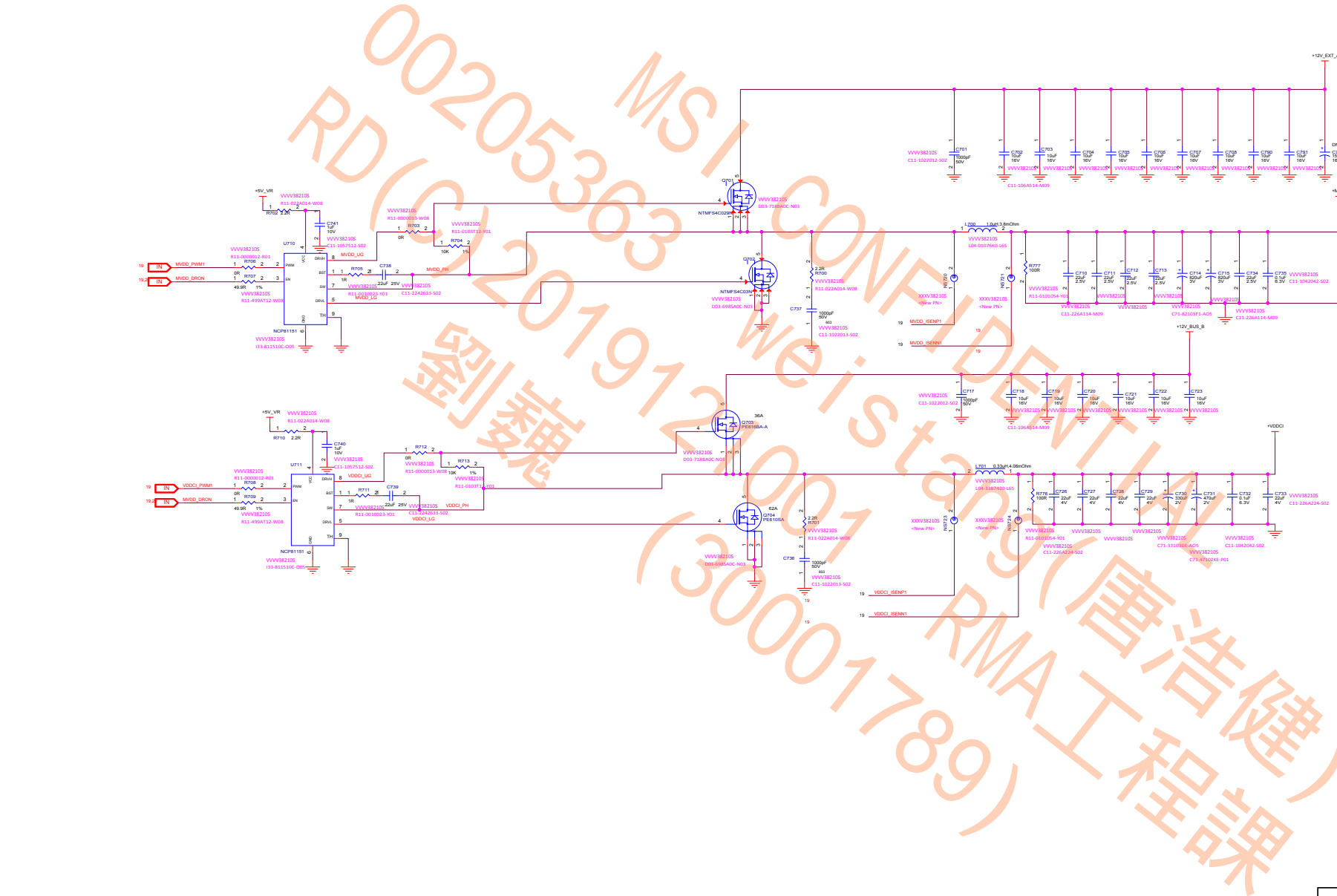
SVC	SVD	BOOT VOLTAGE
0	0	1.1V
0	1	1.0V
1	0	0.9V
1	1	0.8V

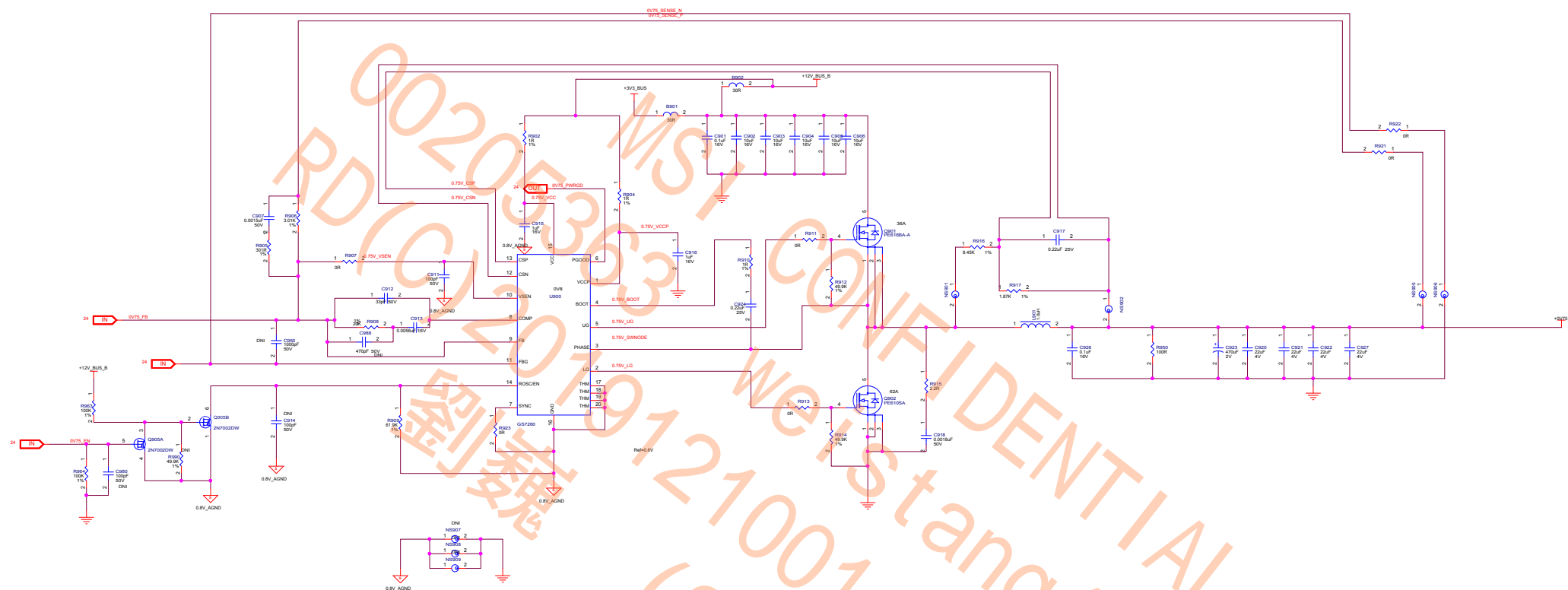




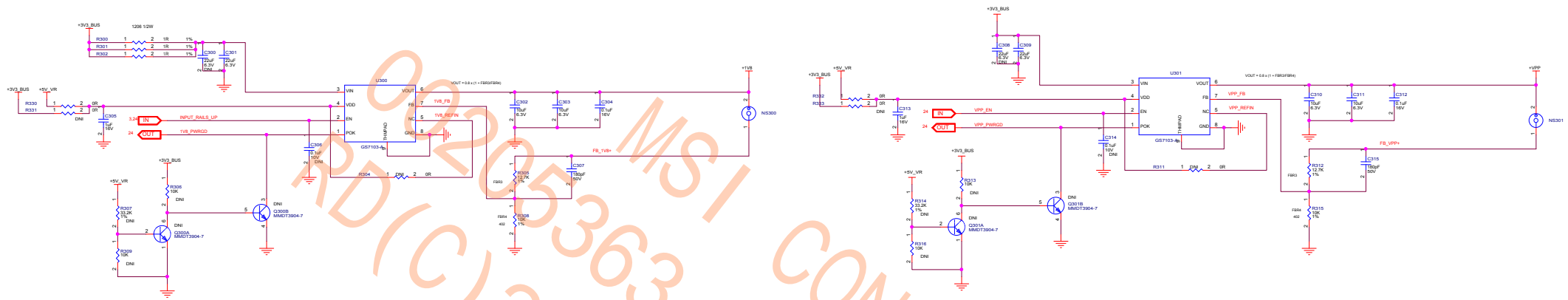


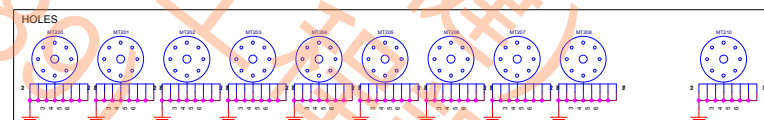
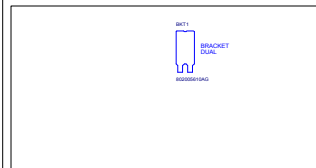
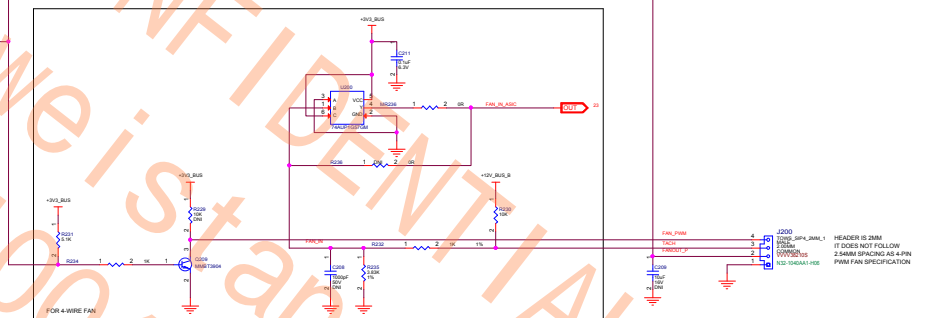
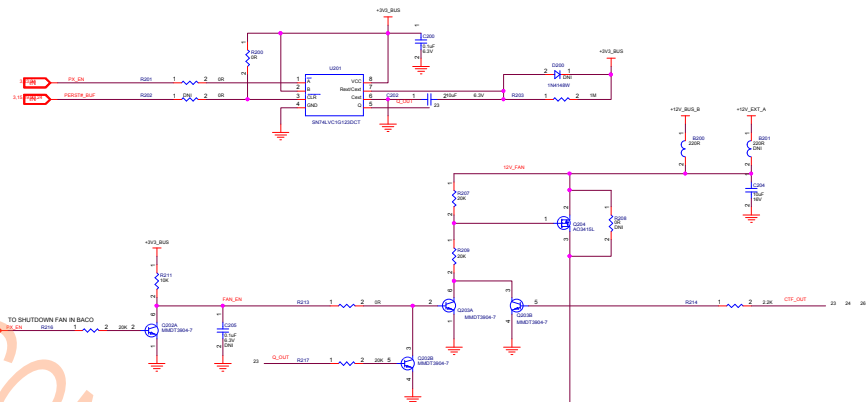


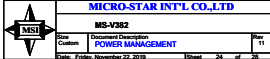


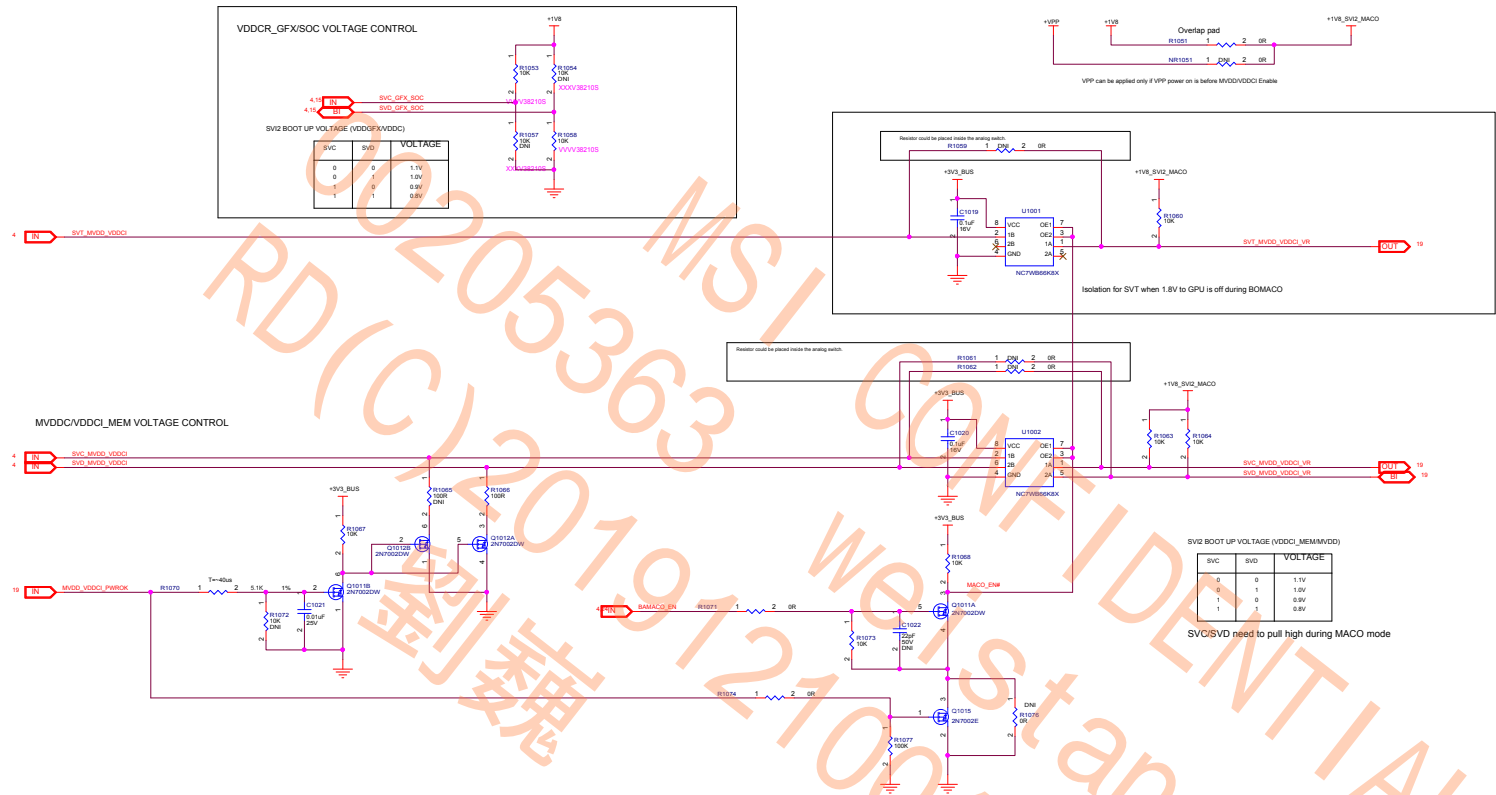


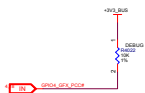
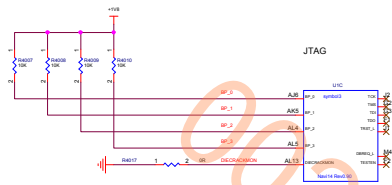
LDO - 1V8/VPP/5V











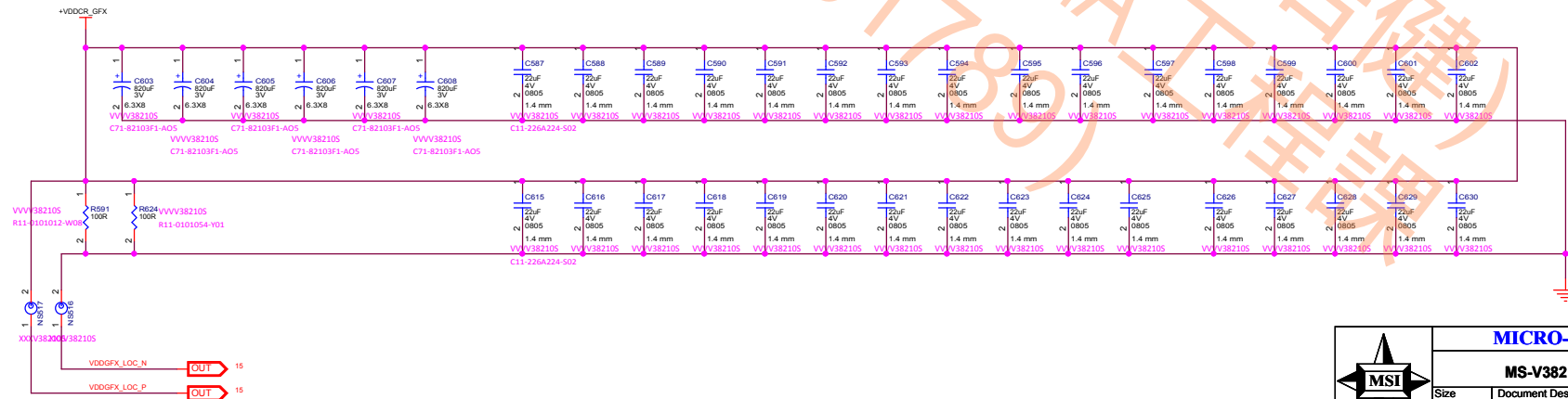
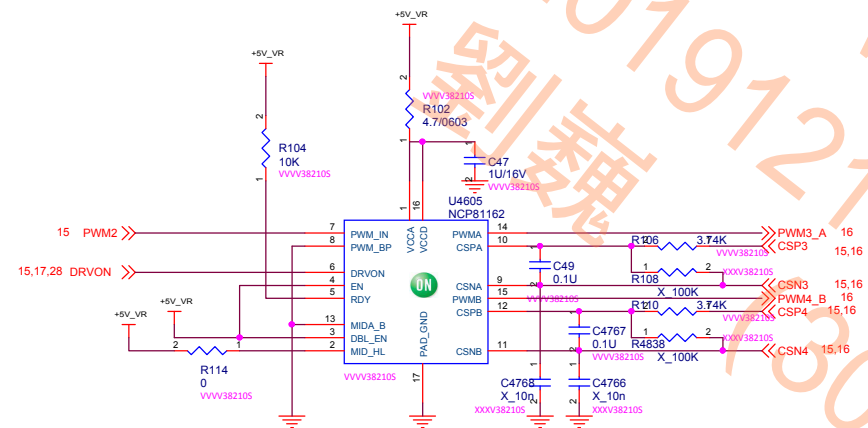
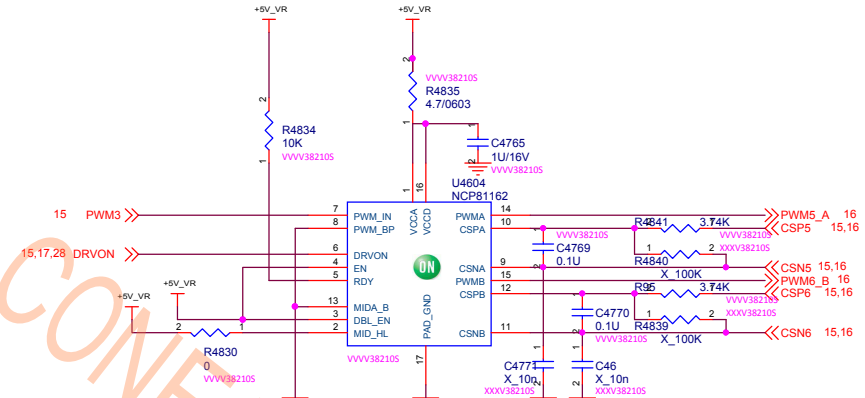
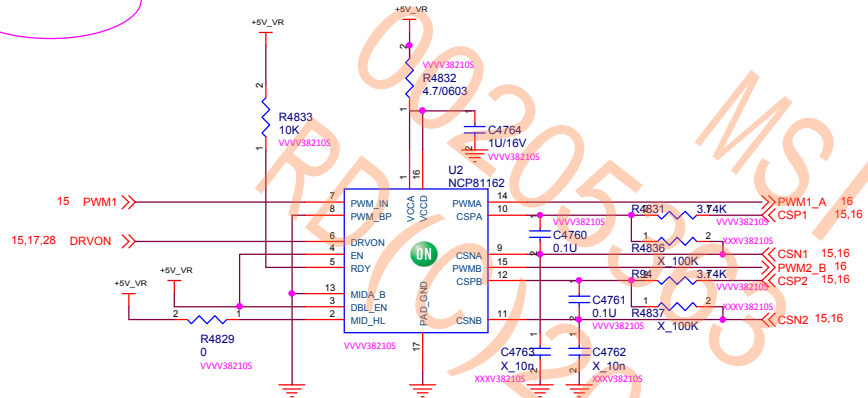
REVISION HISTORY

1.00	00A	08/09/2018	Initial schematic
1.00	00B	12/28/2018	change all 4172010501G parts to 4172010500G
1.00	00B	04/16/2019	ADD D1152 D1162 D1182 D1192 ADD R1152 AND CHANGE R1152 LOCAL SENSE POINT ADD R1162 R1182 R1192 MR1102 MR1103 and reassign gfa phase1-3 input source ADD R4653 R4654 R4655 C4650 Change T01: 0.53uFV250118000G RT49: 800R116000000G NR104: 30K13160300200G RP43: 18K13160180200G CT47:470pF H170047100G CT48:220pF H T10022100G

2019/11/18 10:22:10 - V382 1-FC Change Vdd QF XV P P S O C P W M I F 3 2 1 7 1 A O Z K U R 8 1 0 2 2 Z

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RMA工程課
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MS-V382

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